

End view of PMOD1 connector on a WWU FPGA3 board

Vdd 12	GND 11	10	9	8	7
Vdd 6	GND 5	SCK 4	$\overline{\text{LDAC}}$ 3	SDI 2	$\overline{\text{CS}}$ 1

← DAC signals

← Constraint file  
PMOD1 subscript number

Spartan 6 board FPGA pin # → R12 T12 T14 T13

Artix 7 board FPGA pin # → AB26 AC26 W25 Y26

Note: Spartan 6 pinout is the same for both  
the sdram and ddr3 spartan 6 board versions

WWU FPGA3 board PMOD1 pin out		
WWU pmod board DAC signals		
L.Aamodt		4/28/21