

ENGR-433
HW # 9

In the figure below a logic part is driving five other logic parts. Do the following:

- Determine the high and low logic noise margins for each of the five receiving logic gates. Identify any potential issues.
- Determine the output currents, at both high and low logic levels, for the 74LS00 gate that drives the other 5 gates.
- How many additional 74LS00 gates could be added without exceeding the normal output current of the driving gate?

