

ENGR 355 - Embedded Systems Design
Winter 2022 Schedule

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<u>Date</u>		<u>Topic</u>		<u>Reading</u>	<u>HW due</u>
M	Jan. 3	Introduction			
W	5	NXP KL25Z microcontroller architecture		pg3-23	
F	7	Logic circuit interfacing; noise margin		pg26-29	
M	10	More on logic circuits; dev. board		pg30-36	
W	12	General Purpose I/O; development software		pg37-43	
F	14	More on interfacing		pg43-47	
M	17	Software concurrency, part 1		pg52-59	
W	19	Software concurrency, part 2		pg59-68	
F	21	Software concurrency, part 3		pg68-77	
M	24	ARM Cortex-M0+ core		pg82-98	
W	26	Exceptions & interrupts		pg98-108	
F	28	Exam 1			
M	31	Software for interrupts		pg109-116	
W	2	Software development tools		pg120-129	
F	4	Functions and exception handlers		pg129-138	
M	7	Loops, subroutines, memory access		pg138-148	
W	9	Sampling; Digital-to-analog conversion		pg152-159	
F	11	Analog-to-digital conversion		pg164-174	
M	14	Analog Comparator		pg160-164	
W	16	Timer hardware; timer inputs		pg184-202	
F	18	Timer output modes		pg202-206	
M	21	(no class)			
W	23	Serial communications		pg210-220	
F	25	Exam 2			
M	28	Asynchronous serial communications		pg227-236	
W	2	User interface design			
F	4	Project progress			
M	7	Debugging software			
W	9	Project status reports			
F	11	Wrap up			

Tuesday March 15 Final Exam Time 8:00 - 9:50am
(attendance required)

** Note: The reading shown is to be done prior to the respective class