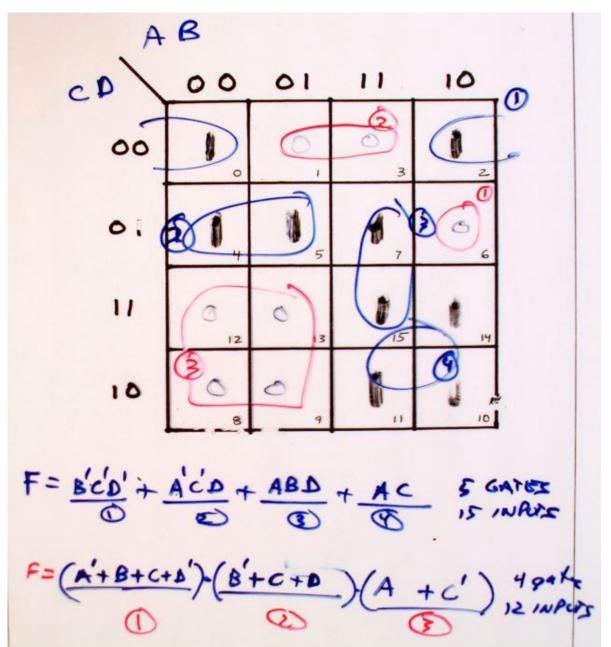


A standard k-map showing both SOP and POS loop-out and resulting "cost" to construct a logic circuit for each.



Blue loops are SOP and red loops are POS