Digital Logic   Homework #10

DUE: Monday, November 4

Objective
Understand the operation of basic sequential circuit elements.

Task
Wire up your design of the PU latch from Homework #9. Report on your success and at least two things you learned in doing this exercise.

Turn In
Staple this assignment sheet to your solutions which are to be done in accordance with the school of engineering homework guidelines posted on the course web page.