Digital Logic  Homework #10

DUE: Monday, November 7

Objective: To understand the operation of basic sequential circuit elements.

Part I

Do the following problems from chapter 7 of your textbook:

7.1
7.7 (Refer to the lecture notes on the web page)
7.34

Part II

Wire up your design of the PU latch from Homework #9. Report on your success and at least two things you learned in doing this exercise.

- **Staple this assignment sheet** to your solutions which are to be done in accordance with the school of engineering homework guidelines posted on the course web page.